



Power Fingerprinting (PFP): Intrusion Detection in Critical Infrastructure using Unintended Analog Emissions

**More Situational Awareness for Industrial Control
Systems (MOSAICS) Industry Day
4-5 November 2020**



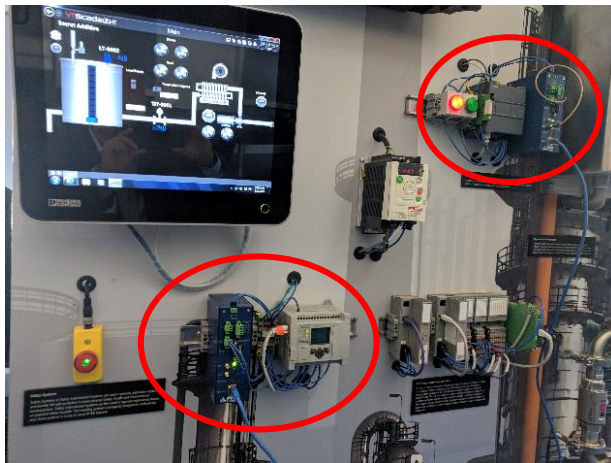
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Bottom Line Up Front

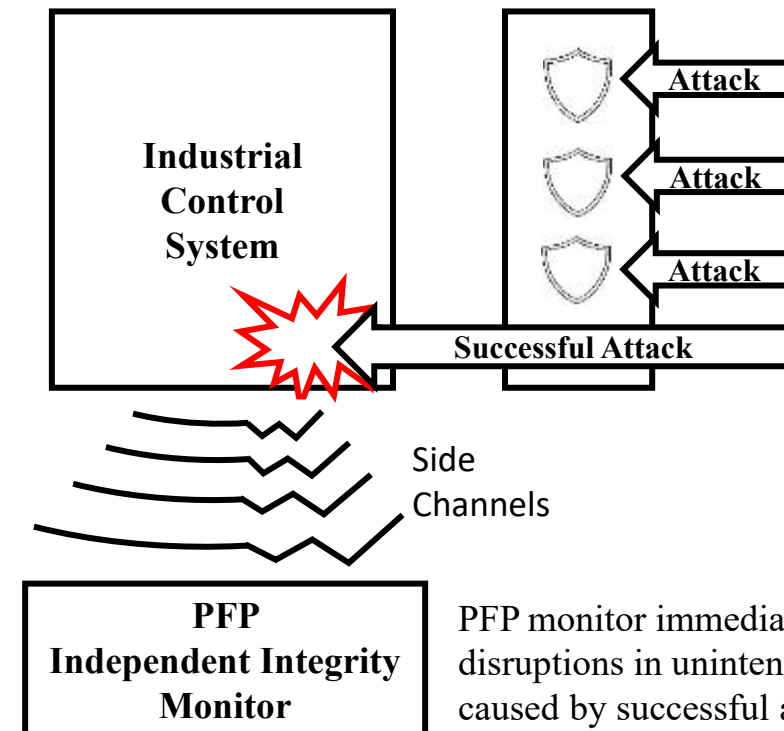


Power Fingerprinting (PFP) enhances situational awareness in critical ICS by using unintended analog emissions to assess the integrity of devices and detect intrusions

- Create baselines using machine learning and detect anomalies in machine time
- Suitable for resource-constrained platforms
- Effective against zero-day attacks
- Logically and physically isolated operation from target platform



Traditional security measures stop a large variety of attack vectors, but eventually an attack will succeed





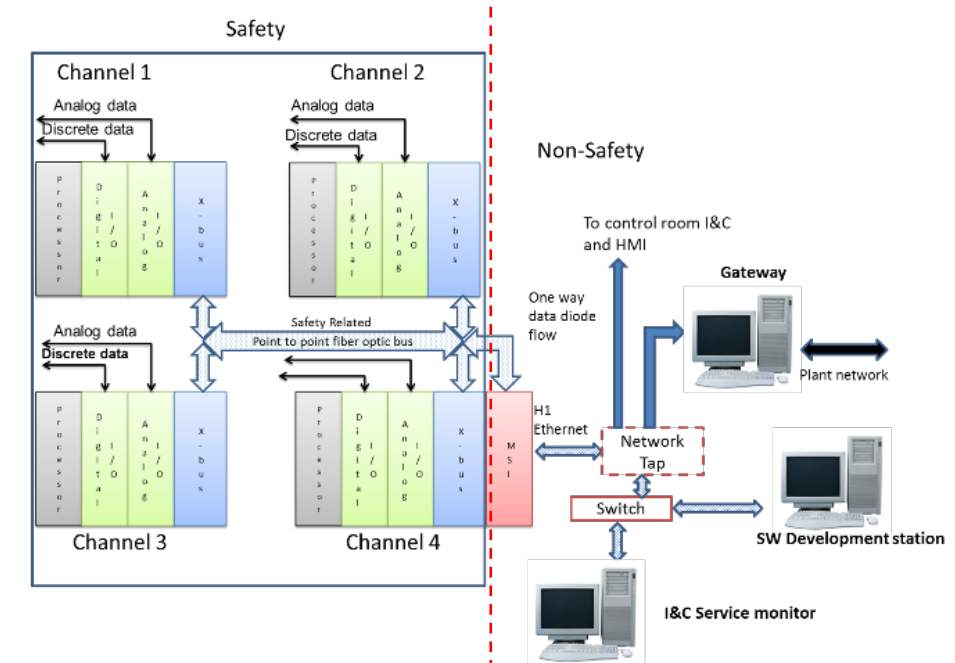
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Cybersecurity in Mission-Critical Systems



- Traditional solutions have limitations for emerging threats in ICS
 - Beyond server and desktops – control, weapons/navigation, and critical systems are at risk, whether they are connected to the Internet or not
- Untrusted supply chain: hardware/firmware tampering
 - Software only solutions cannot reliably detect HW tamper



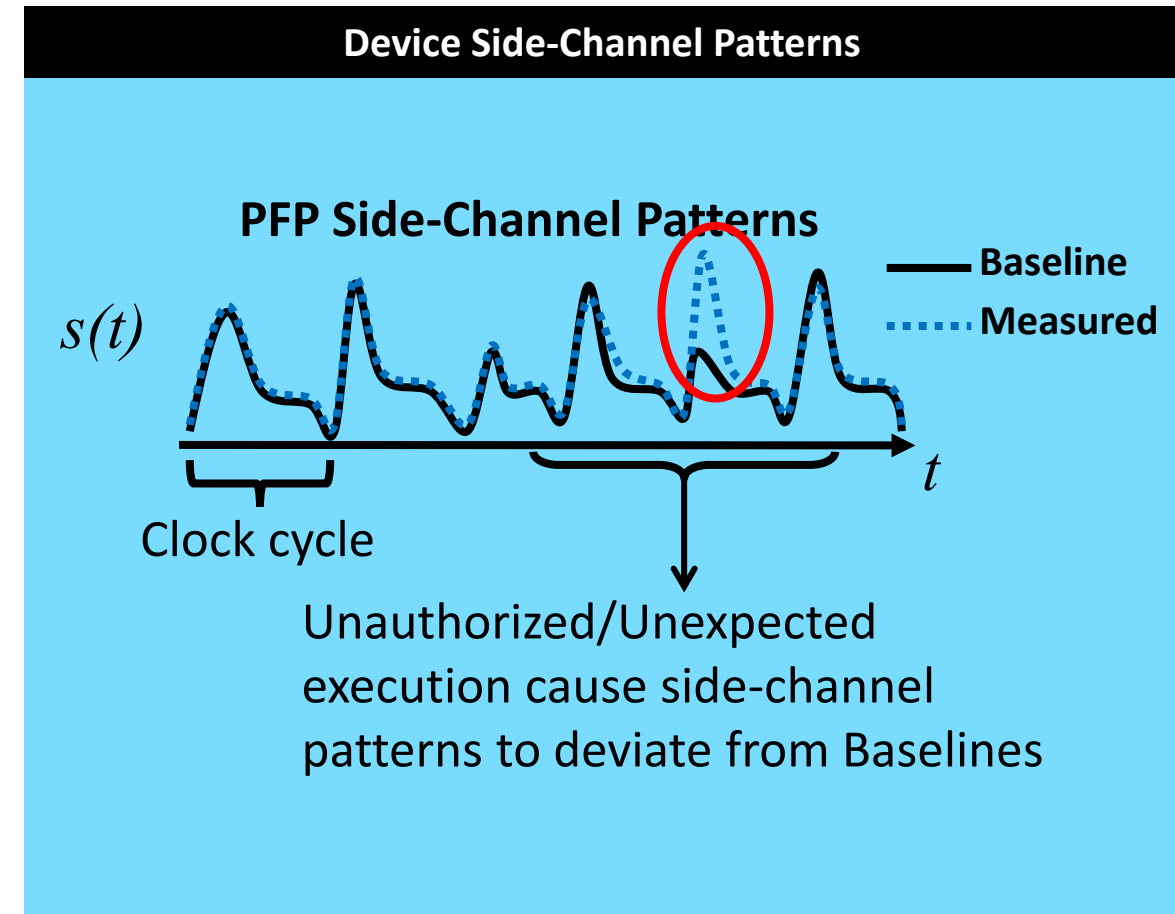
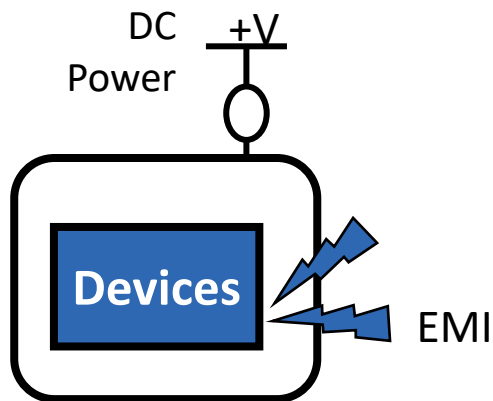
- Strict safety, reliability, & timing requirements
- Legacy systems
- Platform and protocol diversity



Integrity Assessment using Unintended Emissions and Machine Learning

Side channels are unintended analog signals which depend on hardware & firmware and are intrinsic to digital devices

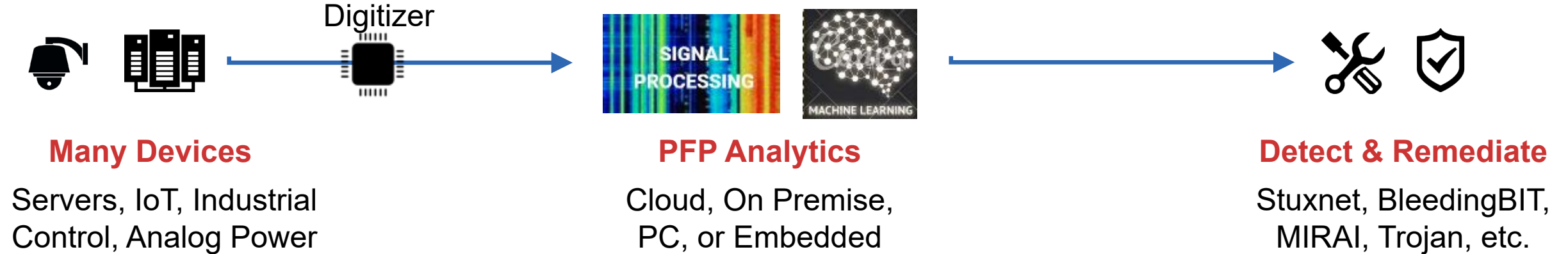
E.g. Power behavior, electromagnetic emissions, temperature, etc.





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How PFP's Technology Works





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PFP System: COTS sensors, Monitors, Analytics



EM Sensors



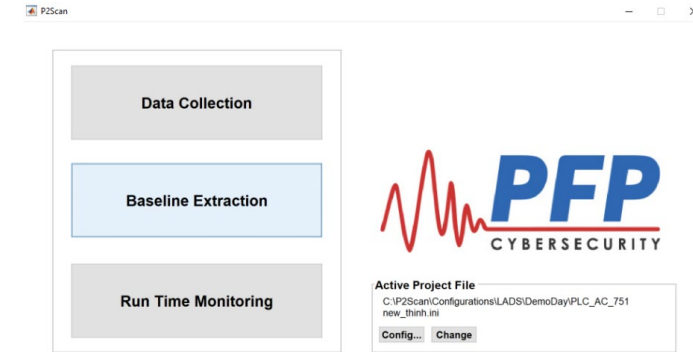
Current Sensors

pMon-751

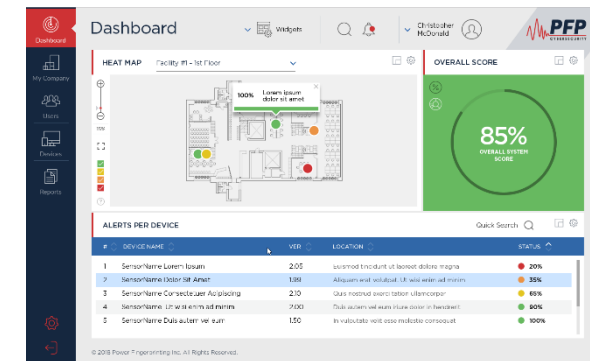


- Low-cost flexible PFP monitor
- EM and DC sensors
 - Collect sensor data
 - Local processing evaluation with trained models (reduce bandwidth requirements)

Third-party sensors



P2Scan – Analytics, Mobile, disconnected



P3Scan – Enterprise Analytics



In progress: M.2 form-factor monitor for rugged servers



arm



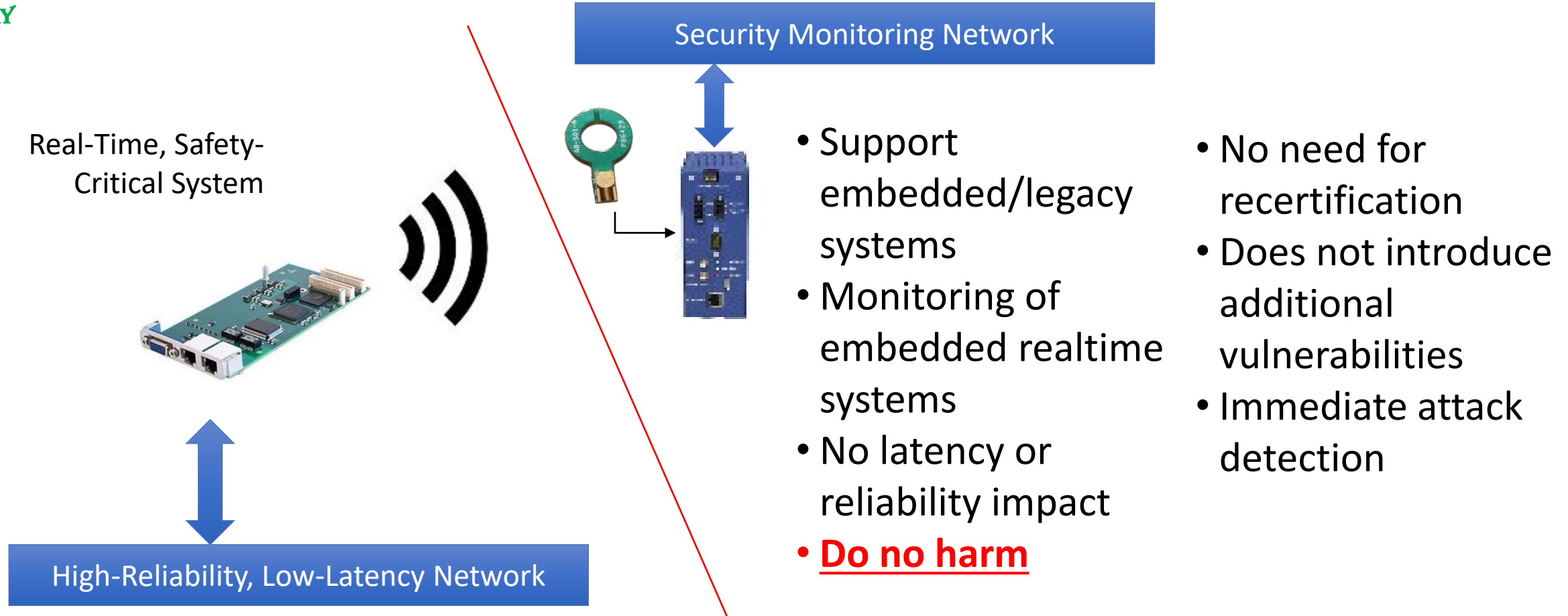
Raspberry Pi



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PFP Impact on Safety-Critical Systems





PFP MOSAICS Fit



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MOSAICS Solution Requirements	PFP Integrity Assessment Technology	
Cyber vulnerability baselining	Baseline execution/logic behavior of ICS devices based on physical unintended emissions and Machine Learning	✓
Enhanced asymmetric threat indications and warnings	Provide threat indicators about the integrity and operational status of ICS Devices being monitored	✓
Anomaly detection	Anomaly detection to detect deviations from the baseline e.g. malicious intrusions, etc.	✓
Information sharing capabilities within an automation framework	Scalable analytics framework to collect and aggregate PFP indicators and share with SIEMs	✓
Enables real-time response actions to disrupt attacker kill chains	Detect violations in machine time (milliseconds)	✓
Timely recovery to restore normal operations	Options for automated response and mitigation	✓
Degrade adversary re-use of attacks	Robust detection capabilities regardless of evasion measures implemented by attacks such as stealth and polymorphism	✓

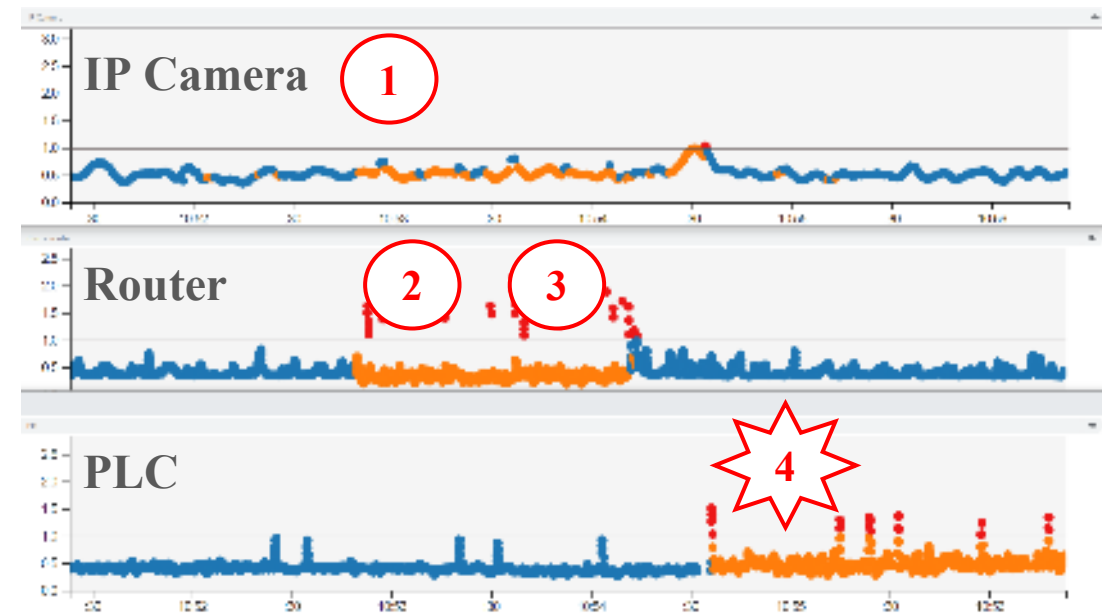
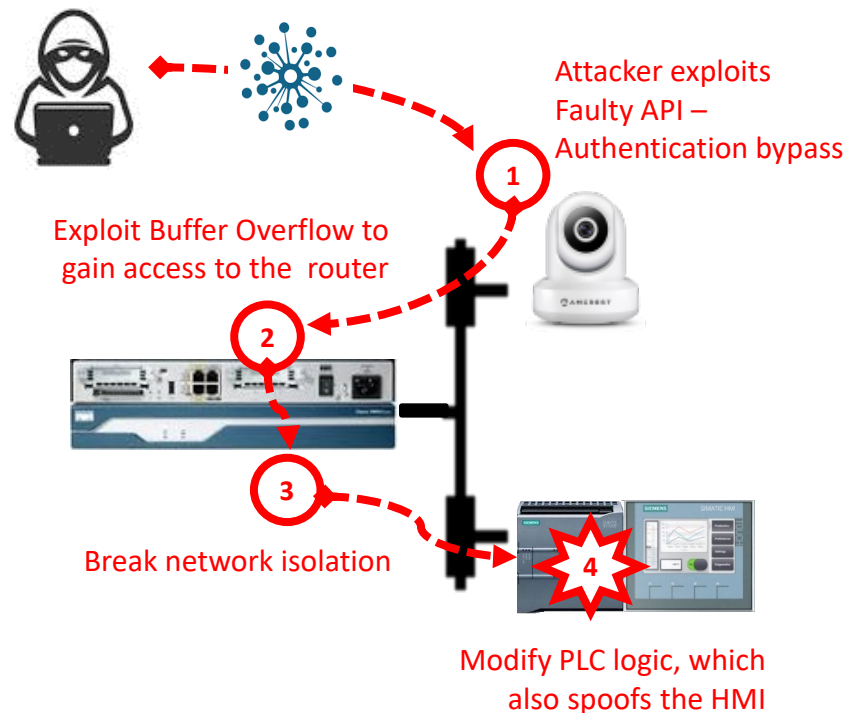


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Real-time Cyber Kill Chain Tracking in Critical Infrastructure



- Simultaneously monitor multiple devices in a critical infrastructure setup and detect attacks in real time to track adversaries' lateral movement.



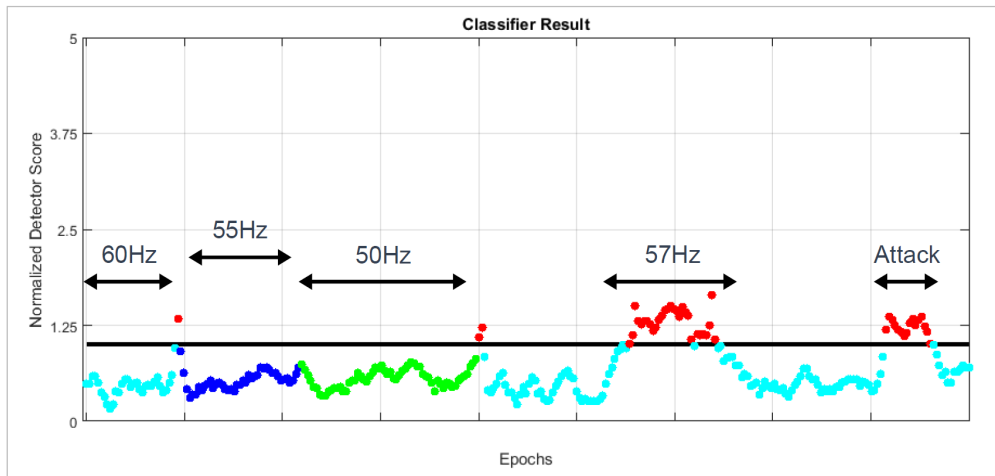
PFP Real-time detection results: Independent PFP monitors detect the individual intrusions and track adversary's lateral movements



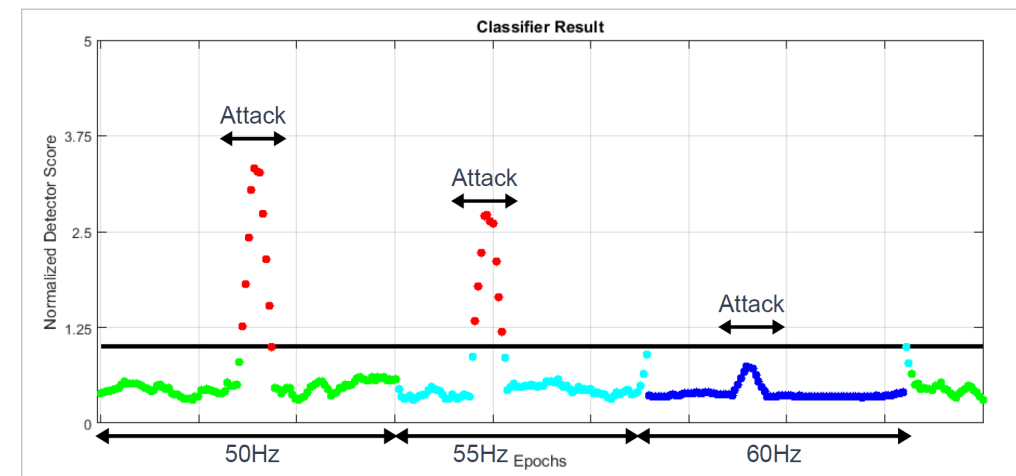
OT Evaluation Testbed: Analog Attacks

- Evaluation setup: multiple attacks on Variable Frequency Drive (VFD)
 - Evaluation performed completely by 3rd party

Attack: Rapid Speed Change



Attack: Rapid Switching frequency Change





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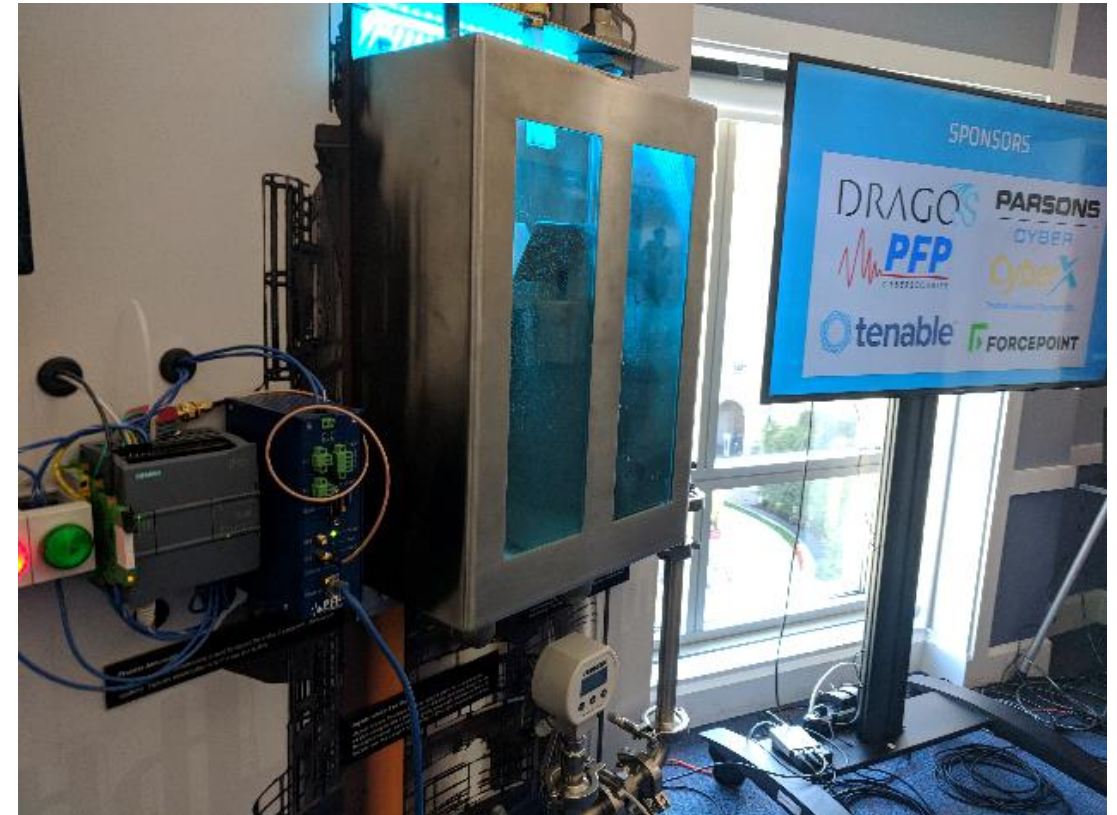
DefCon ICS Village: CTF Monitoring





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DefCon ICS Village: CTF Monitoring



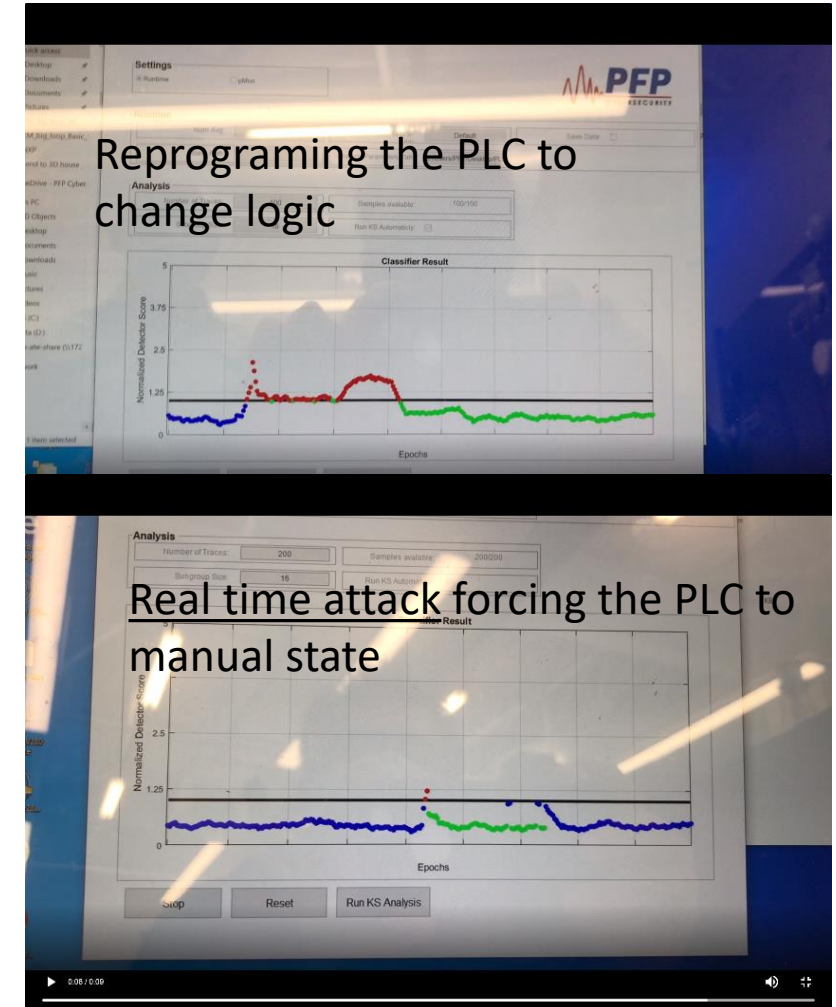
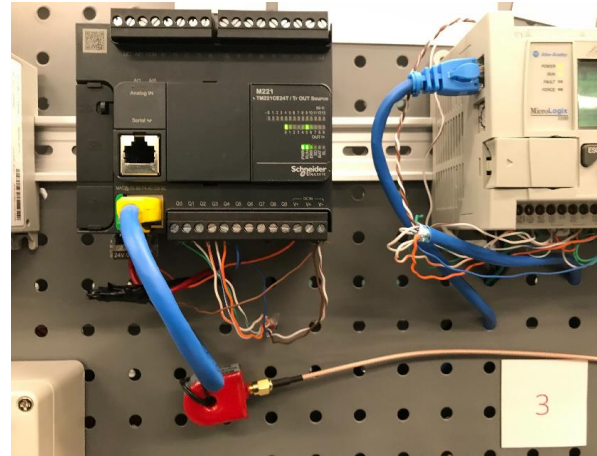


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DreamPort DreamValley RPE: Traffic Controller Real-Time Attack Detection



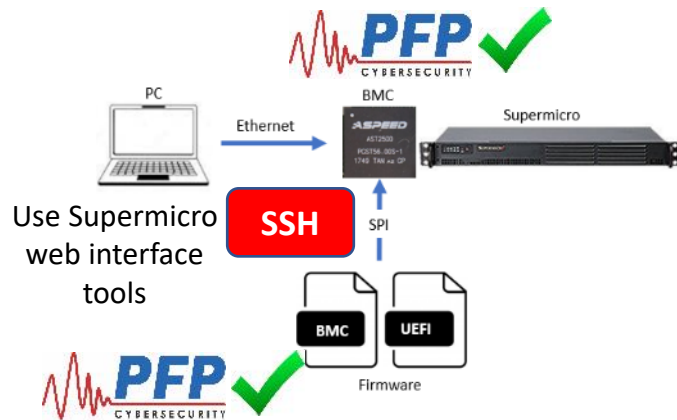
- Defense (BLUE) vs Offense (RED)
- IT and OT solutions to monitor DreamValley infrastructure
- Red Team conduct a coordinated assault against the city





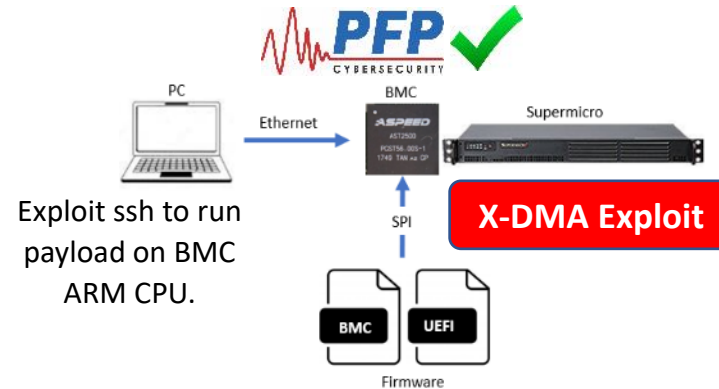
Evaluation BMC Attack

- A sample BMC exploit will attack in three steps, the first is loading a modified firmware, use X-DMA to inject shellcode in CPU kernel, then install backdoor



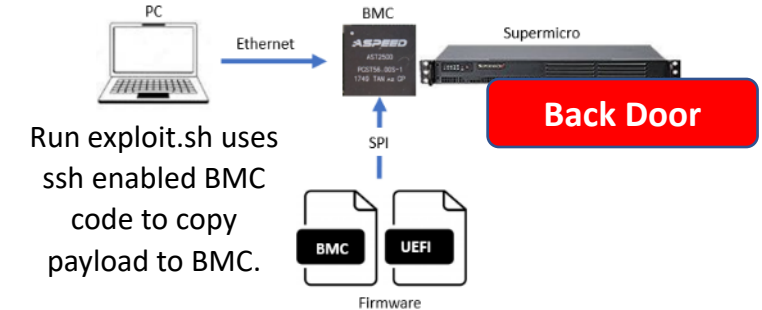
Step 1: Modifies BMC firmware

- Modifies BMC firmware to enable ssh
 - This process is done on a Local PC
 - Enable ssh then copy over the Backdoor exploit
- The modified BMC code is updated on the BMC using Supermicro web interface tools on the Local PC



Step 2: Run exploit script on Local PC

- PFP runs exploit.sh on Local PC
- Exploit.sh copies payload from the Local PC to BMC (using ssh) and executes the payload on the BMC.
- Payload uses X-DMA to inject shellcode into the kernel code



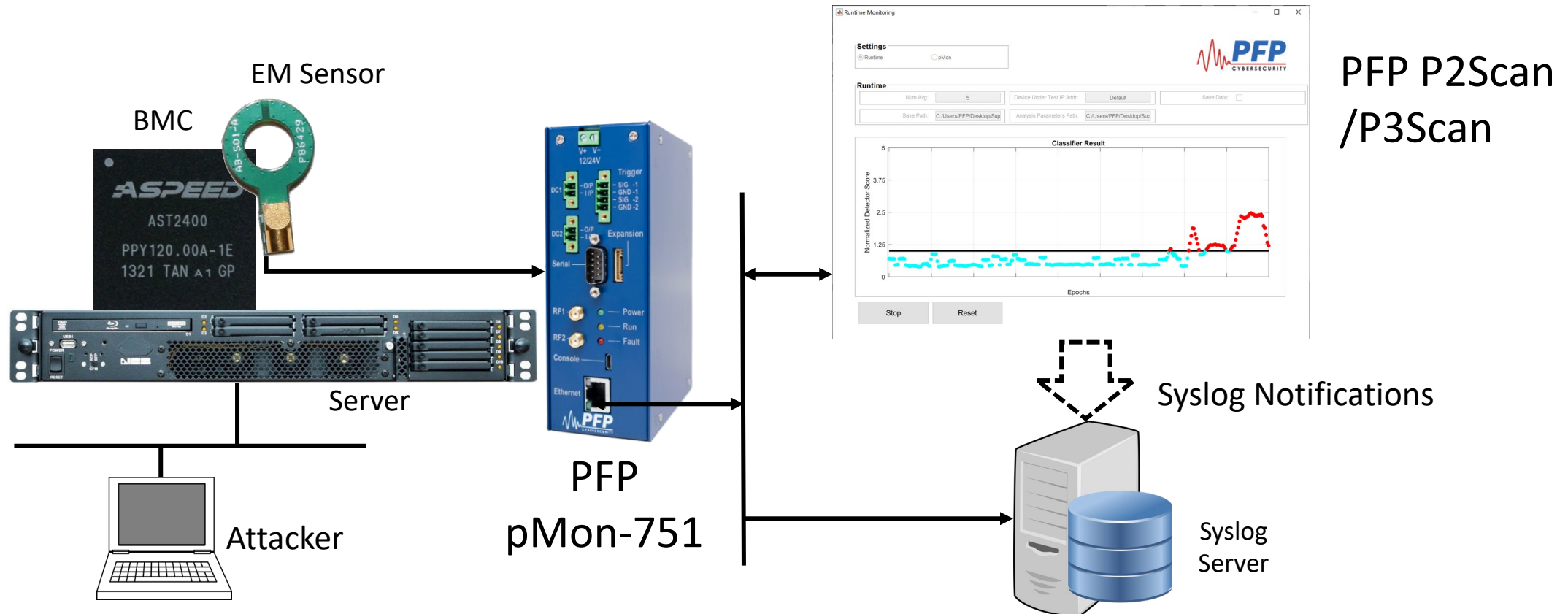
Step 3: Install Backdoor

- Kernel shellcode runs Python with a backdoor command
- Python backdoor connects back to the attacker, providing a shell



BMC exploit in Data Center Server

- Detect Supermicro X10 BMC attack: Load a modified firmware, use X-DMA exploit to inject shellcode in CPU kernel, install backdoor.





Questions?

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