

Steve Carlson
Director, Aerospace and Defense Solutions
Cadence Design Systems, Inc.

A thirty year veteran of the electronics industry, Steve has been focused on Aerospace and Defense system solutions for the past six years.

His current research interests are in hardware and system security, systems verification, digital twinning, advanced semiconductor design processes and approaches to parts obsolescence.

Prior, Steve headed marketing Chief Strategy Office and for the Silicon Realization Group which included the digital, custom/analog and sign-off products. Steve joined Cadence in April 2003 via the Get2Chip acquisition, where he was the VP of Marketing. Prior to Get2Chip, Steve was the CEO of Tharas Systems, a hardware acceleration company. Steve has also held various management positions at Escalade, LSI Logic, United Technologies and Synopsys.

At Synopsys, Steve was a part of the original Design Compiler technical team responsible for timing analysis and optimization.

Steve was the author of the industry's first book on high-level design titled, "Introduction to HDL-based Design Using VHDL".

Steve earned a BSEE, BSCS, and an MSEE from the University of Colorado.

He has authored numerous books, papers, articles related to advanced electronics design.

Steve is a volunteer and supporter of the Lazarex Cancer Foundation (www.lazarex.org).